

FORM PTO-1390 (Modified)
(REV 11-2000)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

TRANSMITTAL LETTER TO THE UNITED STATES

220202US2XPCT

DESIGNATED/ELECTED OFFICE (DO/EO/US)

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR

CONCERNING A FILING UNDER 35 U.S.C. 371

10/069512

INTERNATIONAL APPLICATION NO.
PCT/JP00/05798INTERNATIONAL FILING DATE
28 August 2000PRIORITY DATE CLAIMED
2 September 1999

TITLE OF INVENTION

MAGNETRON PLASMA PROCESSING APPARATUS

APPLICANT(S) FOR DO/EO/US

YAMASAKI Tomomi et al.

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (24) indicated below.
4. ☒ The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. ☒ A copy of the International Application as filed (35 U.S.C. 371 (c) (2))
 - a. ☐ is attached hereto (required only if not communicated by the International Bureau).
 - b. ☒ has been communicated by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☒ An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).
 - a. ☒ is attached hereto.
 - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
 - a. ☐ are attached hereto (required only if not communicated by the International Bureau).
 - b. ☐ have been communicated by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☒ have not been made and will not be made.
8. ☐ An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☒ An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).
10. ☐ An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).
11. ☒ A copy of the International Preliminary Examination Report (PCT/IPEA/409).
12. ☒ A copy of the International Search Report (PCT/ISA/210).

Items 13 to 20 below concern document(s) or information included:

13. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
14. ☒ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
15. ☐ A **FIRST** preliminary amendment.
16. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
17. ☐ A substitute specification.
18. ☐ A change of power of attorney and/or address letter.
19. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.
20. ☐ A second copy of the published international application under 35 U.S.C. 154(d)(4).
21. ☐ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
22. ☐ Certificate of Mailing by Express Mail
23. ☒ Other items or information:

Drawings (5 sheets)/Form PTO-1449

Notice of Priority/Form PTO-1595

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 1.53) 10/069512		INTERNATIONAL APPLICATION NO. PCT/JP00/05798		ATTORNEY'S DOCKET NUMBER 220202US2XPCT	
--	--	--	--	--	--

24. The following fees are submitted:

				CALCULATIONS PTO USE ONLY	
BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) :					
<input type="checkbox"/>	Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO			\$1040.00	
<input checked="" type="checkbox"/>	International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO			\$890.00	
<input type="checkbox"/>	International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO			\$740.00	
<input type="checkbox"/>	International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4)			\$710.00	
<input type="checkbox"/>	International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4)			\$100.00	
ENTER APPROPRIATE BASIC FEE AMOUNT =				\$890.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than months from the earliest claimed priority date (37 CFR 1.492 (e)). <input type="checkbox"/> 20 <input type="checkbox"/> 30				\$0.00	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	12 - 20 =	0	x \$18.00	\$0.00	
Independent claims	3 - 3 =	0	x \$84.00	\$0.00	
Multiple Dependent Claims (check if applicable). <input type="checkbox"/>				\$0.00	
TOTAL OF ABOVE CALCULATIONS =				\$890.00	
<input checked="" type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27). The fees indicated above are reduced by 1/2.				\$0.00	
SUBTOTAL =				\$890.00	
Processing fee of \$130.00 for furnishing the English translation later than months from the earliest claimed priority date (37 CFR 1.492 (f)). <input type="checkbox"/> 20 <input type="checkbox"/> 30				\$0.00	
TOTAL NATIONAL FEE =				\$890.00	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). <input checked="" type="checkbox"/>				\$40.00	
TOTAL FEES ENCLOSED =				\$930.00	
				Amount to be: refunded	\$
				charged	\$

a. ☒ A check in the amount of **\$930.00** to cover the above fees is enclosed.

b. ☐ Please charge my Deposit Account No. _____ in the amount of _____ to cover the above fees. A duplicate copy of this sheet is enclosed.


c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. **15-0030** A duplicate copy of this sheet is enclosed.

d. ☐ Fees are to be charged to a credit card. **WARNING:** Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

Surinder Sachar
Registration No. 34,423



22850

Surinder Sachar

SIGNATURE

Marvin J. Spivak

NAME

24,913

REGISTRATION NUMBER

March 4 2002

DATE

D E S C R I P T I O N

MAGNETRON PLASMA PROCESSING APPARATUS

5

Technical Field

The present invention relates to a magnetron plasma processing apparatus for processing a target substrate such as a semiconductor wafer with a magnetron plasma.

10

Background Art

15

20

In recent years, a magnetron plasma etching apparatus for performing micropattern etching by generating a high-density plasma in a comparatively low-pressure atmosphere has been put into practical use. In this apparatus, a permanent magnet is arranged above a chamber. The magnetic field produced by the permanent magnet is applied horizontally to a semiconductor wafer. An RF electric field perpendicular to the magnetic field is simultaneously applied to the semiconductor wafer. The drift motion of electrons caused upon application of the magnetic and electric fields is utilized to etch the semiconductor wafer with a very high efficiency.

25

In the magnetron plasma, what contributes to the drift motion of electrons is a magnetic field perpendicular to the electric field, i.e., a magnetic field horizontal to the semiconductor wafer.

As a uniform horizontal magnetic field is not always formed in the above apparatus, plasma uniformity is not sufficient. Thus, a nonuniform etching rate, charge-up damage, and the like may occur.

5 In order to avoid these problems, formation of a horizontal magnetic field uniform with respect to the semiconductor wafer in the processing space in the chamber is sought. As a magnet that can generate such a magnetic field, a dipole ring magnet is known.

10 As shown in FIG. 9, this dipole ring magnet 102 is formed by arranging a plurality of anisotropic segment columnar magnets 103 in a ring-like shape outside a chamber 101. The directions of magnetization of the plurality of anisotropic segment columnar magnets 103

15 are slightly shifted from each other to form a uniform horizontal magnetic field B as a whole. FIG. 9 is a view (plan view) of the apparatus seen from above. The proximal end side of the direction of the magnetic field is indicated by N, the distal end side thereof is

20 indicated by S, and positions separated 90° from N and S are respectively indicated by E and W. In FIG. 9, reference numeral 100 denotes a semiconductor wafer.

 The horizontal magnetic field formed by the dipole ring magnet in this manner is a horizontal magnetic

25 field directed only in one direction of from N to S in FIG. 9. Accordingly, electrons travel in one direction by drift motion, causing nonuniformity in the plasma

density. More specifically, the electrons drift in the direction of the outer product of the electric field and the magnetic field. In other words, when the electric field is formed to extend downward from above, the electrons travel from the E pole side to the W pole side by drift motion. Consequently, the plasma density is low on the E pole side and high in the W pole side, resulting in nonuniformity.

To prevent this, the dipole ring magnet is rotated in its circumferential direction to form a rotating magnetic field, so the direction of drift motion of electrons is changed. In practice, however, this cannot make the plasma density sufficiently uniform over a wide range.

A technique has been proposed in which a magnetic field gradient is formed in the drift direction of electrons, i.e., in a direction from the E pole side to the W pole side in FIG. 9, so the nonuniformity of plasma accompanying the drift motion of electrons is eliminated (Jpn. Pat. Appln. KOKAI Publication No. 9-27278). According to this technique, as shown in FIG. 10, the number of anisotropic segment columnar magnets on the W pole side is made smaller than that on the E pole side. As a result, a gradient is formed in which the magnetic field strength increases in the direction from the E pole side to the W pole side.

In recent years, devices have been required to

shrink more and more in feature size, and a plasma etching process in a lower pressure has been required. To perform an efficient plasma process in a low pressure, the plasma density must be further increased.

5 For this purpose, in the magnetron plasma process, the magnetic field strength may be increased.

When, however, an insulating film such as an oxide film is to be etched, to avoid charge-up damage, the magnetic field strength of that portion where a wafer is present is limited to about 200 Gauss at maximum.

10 Even when a magnetic field gradient is formed by the above technique and plasma uniformity can be achieved, the plasma density cannot be sufficiently increased, and the etching rate may not be sufficiently high.

15 If a region where the magnetic field is locally strong can be formed outside the E pole-side end of the wafer, the plasma density may be increased without damaging the wafer. Conventionally, however, it is difficult to locally form a strong magnetic field portion.

20 Disclosure of Invention

It is an object of the present invention to provide a magnetron plasma processing apparatus which can form a locally strong magnetic field portion at a desired position in a processing space for a target

25 object, so the plasma density of the processing space can be increased.

According to a first aspect of the present

invention, there is provided a magnetron plasma processing apparatus which comprises

a chamber with an outer wall that can maintain a reduced pressure,

5 process gas supply means for supplying a process gas into the chamber,

a pair of electrodes arranged in the chamber to oppose each other and to define a processing space therebetween,

10 electric field forming means for applying a voltage to the pair of electrodes, thus forming an electric field in the processing space, and

magnetic field forming means for forming, in the processing space, a magnetic field perpendicular to the
15 direction of the electric field and directed in one direction, and which subjects a target substrate to a magnetron plasma process while the target substrate is set in the processing space to be parallel to the electrodes,

20 the magnetic field forming means having a dipole ring magnet including a plurality of first anisotropic segment magnets arranged in a ring-like shape around the outer wall of the chamber so as to form a magnetic field gradient, within a plane perpendicular to the
25 direction of the electric field, such that a magnetic field strength is large and small on upstream and downstream sides, respectively, in an electron drift

direction along a direction perpendicular to
a direction of the magnetic field,

at least one second anisotropic segment magnet,
arranged in the vicinity of a predetermined region
5 located outside that end of the target substrate which
is on the upstream side in the electron drift
direction, with an N pole thereof being directed toward
the predetermined region, and

at least one third anisotropic segment magnet
10 arranged in the vicinity of the predetermined region
with an S pole thereof being directed toward the
predetermined region,

the second and third anisotropic segment magnets
serving to locally increase the magnetic field strength
15 of the predetermined region to be larger than that
formed by the first anisotropic segment magnets.

According to a second aspect of the present
invention, there is provided a magnetron plasma
processing apparatus which comprises

20 a chamber which has an outer wall and can maintain
reduced pressure,

process gas supply means for supplying a process
gas into the chamber,

a pair of electrodes arranged in the chamber to
25 oppose each other and to define a processing space
therebetween,

electric field forming means for applying

10069512-030402

a voltage to the pair of electrodes, thus forming an electric field in the processing space, and

magnetic field forming means for forming, in the processing space between the pair of electrodes, a magnetic field perpendicular to the direction of the electric field and directed in one direction, and which subjects a target substrate to a magnetron plasma process while the target substrate is set in the processing space to be parallel to the electrodes,

the magnetic field forming means having a dipole ring magnet including a large number of anisotropic segment magnets arranged in a ring-like shape around the outer wall of the chamber, to form a magnetic field gradient, within a plane perpendicular to the direction of the electric field between the electrodes, such that the magnetic field strength is large and small on upstream and downstream sides, respectively, in an electron drift direction along a direction perpendicular to the direction of the magnetic field, and

the plurality of anisotropic segment magnets including a first portion including at least one anisotropic segment magnet arranged in the vicinity of a first region located outside that end of the target substrate which is on the upstream side in the electron drift direction with an N pole thereof being directed toward the first region, and a second portion including

at least one anisotropic segment magnet, arranged in the vicinity of a second region located outside that end of the target substrate which is on the upstream side in the electron drift direction, to be away from the first region, with an S pole thereof being directed toward the second region, the first and second portions serving to locally increase magnetic field strengths of the first and second regions.

With the above arrangement, a strong magnetic field portion can be locally formed at a predetermined region located outside that end of the target substrate which is on the upstream side in the electron drift direction. The plasma density of the processing space can accordingly be increased.

Preferably, the anisotropic segment magnets are arranged such that gaps among those which are on the upstream side in the electron drift direction are narrower than gaps among those which are on the downstream side in the electron drift direction.

The anisotropic segment magnets preferably form a magnetic field with 200 Gauss at maximum, preferably 100 Gauss to 200 Gauss, at a portion thereof opposing the target substrate, and a magnetic field of at least 200 Gauss, preferably 200 Gauss to 400 Gauss, at the predetermined region.

Brief Description of Drawings

FIG. 1 is a sectional view showing a magnetron

plasma etching apparatus according to an embodiment of the present invention;

FIG. 2 is a horizontal sectional view of a dipole ring magnet arranged around the chamber of the apparatus of FIG. 1;

FIG. 3 is a view schematically showing the dipole ring magnet and a state in the chamber of the magnetron plasma etching apparatus of FIG. 1;

FIG. 4 is a schematic view showing the drift state of electrons generated by a focus ring in the magnetron plasma etching apparatus according to the embodiment of the present invention;

FIG. 5 is a view showing the magnetic field strength distribution of a magnetic field formed by the dipole ring magnet of FIG. 2;

FIG. 6 is a view showing the magnetic field strength distribution of a magnetic field formed by a conventional dipole ring magnet;

FIG. 7 is a graph showing the magnetic field strength gradient in the E - W section of the processing space of the magnetron plasma etching apparatus according to the present invention and that of a conventional magnetron plasma etching apparatus;

FIG. 8 is a horizontal sectional view of a dipole ring magnet arranged around the chamber of a magnetron plasma etching apparatus according to another embodiment of the present invention;

FIG. 9 is a schematic view showing a conventional dipole ring magnet; and

FIG. 10 is a schematic view showing a conventional dipole ring magnet.

5 Best Mode for Carrying Out of the Invention

The embodiments of the present invention will be described with reference to the accompanying drawings.

FIG. 1 is a sectional view showing a magnetron plasma etching apparatus according to an embodiment of the present invention. This etching apparatus has an airtight chamber 1. The chamber 1 forms a stepped cylindrical shape constituted by a small-diameter upper portion 1a and large-diameter lower portion 1b. The wall of the chamber 1 is made of a conductor such as aluminum. A support table 2 for horizontally supporting a semiconductor wafer 30 as a target object thereon is arranged in the chamber 1. For example, the support table 2 is formed of an aluminum circular plate, and is supported on a conductive support base 4 through an insulating plate 3. A conductive focus ring 5 made of, e.g., single-crystal silicon, is arranged on the periphery of an upper surface of the support table 2. The support table 2 and support base 4 can be vertically moved with respect to the chamber 1 by a known ball screw mechanism in directions indicated by arrows. The ball screw mechanism has at least one (two in this embodiment) ball screw 7 and a motor

(not shown) for rotating the screw. A driving portion under the support base 4 is covered by a stainless steel (SUS) bellows 8 set between the support base 4 and the bottom wall of the chamber 1. A bellows cover 9 formed of a pair of slidable cylinders is arranged outside the bellows 8.

The support table 2 is connected to an RF power supply 10 through a matching box, i.e., a circuit 11. The RF power supply 10 supplies RF power of, e.g., 13.56 MHz, to the support table 2. A shower head 16 (to be described later) is arranged above the support table 2 to oppose and be parallel to it. The shower head 16 makes up the upper portion of the small-diameter portion of the chamber 1. The shower head 16 is grounded. Thus, the shower head 16 and support table 2 serve as a pair of parallel electrodes.

An electrostatic chuck 6 for electrostatically chucking the semiconductor wafer 30 with a diameter of 300 mm is arranged on the surface of the support table 2. The electrostatic chuck 6 is surrounded by the focus ring 5. The electrostatic chuck 6 is constituted by a pair of insulators 6b, and an electrode 6a interposed between them and connected to a DC power supply 12. The power supply 12 applies a voltage to the electrode 6a, so that the semiconductor wafer 30 can be chucked on the electrostatic chuck with a Coulomb force.

A refrigerant channel (not shown) is formed in the support table 2. When an appropriate refrigerant is circulated in the refrigerant channel, the semiconductor wafer 30 can be controlled to a predetermined temperature. A baffle plate 13 is formed outside the focus ring 5 and electrically connected to the chamber 1 through the support base 4 and bellows 8.

The shower head 16 has a lower plate 16a with a large number of gas discharge holes 18. A gas inlet portion 16b is formed at the center of the upper wall of the chamber 1 which is located above the lower plate 16a. A space 17 is formed in the shower head, i.e., between the lower plate 16a and the upper wall. The gas inlet portion 16b is connected to one end of a gas supply pipe 15a. The other end of the gas supply pipe 15a is connected to a process gas supply system 15 for supplying a process gas. The process gas consists of an etching reaction gas and diluent gas. As the reaction gas, a halogen-based gas can be used. As the diluent gas, a gas ordinarily used in this field, e.g., Ar gas, He gas, or the like can be used.

The process gas is supplied from the process gas supply system 15 through the gas supply pipe 15a and gas inlet portion 16b in the space 17 of the shower head 16, and then discharged from the gas discharge holes 18 to etch a film formed on the semiconductor

wafer 30.

An exhaust port 19 is formed in the side wall of the lower portion 1b of the chamber 1, and is connected to an exhaust system 20. When a vacuum pump provided in the exhaust system 20 is actuated, the interior of the chamber 1 can be reduced to a predetermined vacuum degree. A gate valve 24 is provided on the upper side of the side wall of the lower portion 1b of the chamber 1. The gate valve 24 opens and closes a loading/unloading port for the semiconductor wafer 30.

A dipole ring magnet 21 is concentrically arranged around the upper portion 1a of the chamber 1. The dipole ring magnet 21 forms a horizontal magnetic field in the processing space between the support table 2 and shower head 16. The dipole ring magnet 21 can be rotated by a rotating means such as a motor (not shown) about the central axis of the upper portion 1a as the center.

In the magnetron plasma etching apparatus with the above arrangement, first, the gate valve 24 is opened. The semiconductor wafer 30 is loaded into the chamber 1 and placed on the support table 2. At this time, the DC power supply 12 applies a predetermined voltage to the electrode 6a of the electrostatic chuck 6. The semiconductor wafer 30 is chucked on the electrostatic chuck with a Coulomb force. After this, the support table 2 is moved upward to the position

shown in FIG. 1. The vacuum pump of the exhaust system 20 evacuates the interior of the chamber 1 through the exhaust port 19.

After the interior of the chamber 1 reaches the predetermined vacuum degree, a predetermined process gas is introduced into the chamber 1 from the process gas supply system 15. The interior of the chamber 1 is thus held at a predetermined pressure, e.g., 50 mTorr. In this state, the RF power supply 10 supplies RF power to the support table 2. For example, the RF power has a frequency of 13.56 MHz and a power of 1,000 W to 5,000 W.

In this case, the RF power is applied in the above manner to the support table 2 serving as a lower electrode. Thus, a vertical electric field is formed in the processing space between the shower head 16 serving as the upper electrode and the support table 2 serving as the lower electrode. A horizontal magnetic field is formed in the upper portion 1a of the chamber 1 by the dipole ring magnet 21. Therefore, magnetron discharge occurs in the process gas due to the drift of electrons, thus forming a process gas plasma. The process gas plasma etches a predetermined film formed on the semiconductor wafer 30.

The dipole ring magnet will be described.

FIG. 2 is a cross-sectional view of the dipole ring magnet arranged around the chamber 1.

As shown in FIG. 2, the dipole ring magnet 21 is formed by attaching a large number of anisotropic segment magnets 22 to a ring-like magnetic casing 23. Each anisotropic segment magnet 22 is formed of a columnar magnet with a magnetizing direction in its radial direction. In this embodiment, 30 anisotropic segment magnets 22 are arranged in the ring-like shape. However, the number of anisotropic segment magnets is not limited to this. The sectional shape of each anisotropic segment magnet is not limited to a circle as in the embodiment, but an arbitrary shape can be employed, e.g., a square, rectangle, or trapezoid. The magnet material that forms the anisotropic segment magnets 22 is not limited particularly, and a known magnet material can be employed, e.g., a rare earth-type magnet, a ferrite-type magnet, or an Alnico magnet. The arrangement of such a dipole ring magnet and the rotating mechanism for it are disclosed in, e.g., U.S.P. No. 6,014,943 described herein as a reference.

In the dipole ring magnet 21, the directions of magnetization of the plurality of first-group first anisotropic segment magnets 22 are slightly shifted from each other in the same manner as in the prior art. Thus, the dipole ring magnet 21 has first-group first anisotropic segment magnets 22 that form a horizontal magnetic field B directed in one direction as a whole,

as shown in FIG. 2. In FIG. 2, the proximal end side of the direction of the magnetic field is shown as an N pole side, the distal end side thereof is shown as an S pole side, and positions shifted by 90° from the N and S end sides are respectively shown as E and W poles.

As shown in FIG. 3, a vertical electric field EL and the horizontal magnetic field B extending almost from the N pole side toward the S pole side are formed in the processing space. Thus, electrons generated by the plasma travel from the E pole side toward the W pole side by drift motion. Therefore, the electron density on the W side will increase to cause nonuniformity in plasma density. The larger the magnetic field strength, the larger the plasma density. When a magnetic field strength gradient is formed such that the magnetic field strength on the E pole side is large and that on the W pole side is small, the above problem can be solved.

For this purpose, conventionally, those anisotropic segment magnets which are on the W pole side portion are thinned out to be less than those on the E pole side. Also, the magnetic strength of the anisotropic segment magnets on the E pole side is increased. In this manner, the magnet is designed to form lines of magnetic force with which the magnetic field strength on the E pole side end of the processing space is largest and the drift electrons diffuse on

the W pole side.

When the insulating film (oxide film, nitride film, or the like) on the semiconductor wafer 30 is to be etched, the magnetic field strength must be set to 200 Gauss or less even at the E pole side end where it is largest on the semiconductor wafer 30, because of possible charge-up damage. With a conventional magnetic gradient, the magnetic field strength outside the E pole side end of the wafer is not sufficiently large. Hence, the amount of generated plasma may decrease and the etching rate may not be sufficiently high. In other words, as the feature size of the devices shrinks in recent years, a plasma etching process under a lower pressure is required. To perform an efficient plasma process under a low pressure, the plasma density must be further increased. With the conventional magnetic field gradient, however, a sufficient amount of plasma cannot be generated.

In order to solve this problem, in the dipole ring magnet 21, as shown in FIG. 2, those anisotropic segment magnets which are on the W pole side portion are thinned out to decrease the magnetic field strength of the W pole side. Also, the magnetic poles of the anisotropic segment magnets at portions indicated by reference numerals a and b in FIG. 2, in the vicinity of a region A are directed toward the region A. These anisotropic segment magnets are second and third

anisotropic segment magnets. More specifically, in the anisotropic segment magnets at the portion a, the N poles are directed toward the region A. In the anisotropic segment magnets at the portion b, the S poles are directed toward the region A. When the N and S poles of the anisotropic segment magnets are directed toward the region A in this manner, the magnetic field of the region A can be locally strengthened. When the magnetic field strength of a region outside the E pole side end of the semiconductor wafer 30 is increased in this manner, the amount of plasma generated at this region can be increased. Thus, the plasma density can be increased.

More specifically, assume that the magnetic field strength of a region outside the E pole side of the semiconductor wafer 30 is increased to preferably 200 Gauss or more. Then, as shown in FIG. 4, the amount of electrons generated on the surface of the focus ring 5 outside the E side of the semiconductor wafer 30 increases. Thus, the amount of electrons to be supplied to the W side by drift increases, and accordingly the plasma density increases.

FIG. 5 shows the magnetic field strength distribution formed when the magnetic poles of the anisotropic segment magnets at the portions indicated by a and b are directed to the region A according to the present invention, as shown in FIG. 2.

FIG. 6 shows the magnetic field strength obtained when the directions of these magnetic poles are not changed. In FIGS. 5 and 6, the isomagnetics are plotted at a spacing of 10 Gauss. A comparison of FIGS. 5 and 6 reveals the following. In FIG. 5, the spacings between the isomagnetics are dense at a portion (above the center) corresponding to the region A. The gradient of the magnetic field strength is thus steep, and the magnetic field strength is large at this portion. In contrast to this, in FIG. 6, the spacings between the isomagnetics at a portion corresponding to the region A are not very dense. The gradient of the magnetic field strength is thus moderate, and the magnetic field strength at this portion is not very large. An actual magnetic field strength gradient in an E - W section is as shown in FIG. 7. According to the present invention, the gradient of the magnetic field strength is steep at a region outside the E side end of the semiconductor wafer 30, and a magnetic field strength considerably larger than that of the prior art can be obtained.

A portion with a large magnetic field strength as described above is preferably formed over a wide range outside the E pole side of the semiconductor wafer 30. An isomagnetic strength may then be obtained over a wide range along the E pole side end of the semiconductor wafer 30 and its periphery.

Then, electrons start drifting simultaneously over the wide range in the vicinity of the end region of the semiconductor wafer 30. Therefore, the plasma density can be increased easily.

5 From this viewpoint, as shown in FIG. 8, it is also effective to strengthen the magnetic fields of regions C and D on the two sides of an E - W line outside the E side end of the semiconductor wafer 30. In this case, as shown in FIG. 8, the magnetic pole of
10 at least one (three in this example) anisotropic segment magnet of a portion c in the vicinity of the region C may be directed toward the region C. Also, the magnetic pole of at least one (three in this
15 example) anisotropic segment magnet of a portion d in the vicinity of the region D may be directed toward the region D. Then, a desired magnetic field strength distribution can be obtained. More specifically, the N poles of the three anisotropic segment magnets at the
20 portion c are directed toward the region C. Also, the S poles of the three anisotropic segment magnets at the portion d are directed toward the region D. In this manner, the N and S poles of the anisotropic segment magnets are directed toward the regions C and D,
25 respectively. Then, in these regions, of the magnetic fields formed by the dipole ring magnet, those of the regions C and D can be locally strengthened.

The present invention is not limited to the above

embodiments but can be changed in various ways.

For example, in the above embodiments, a semiconductor wafer with an oxide film is used as a target substrate.

However, the present invention is not limited to this,

5 and other target substrates, e.g., an LCD substrate or a glass substrate with an insulating film, may be used.

In the above embodiments, the present invention is applied to a magnetron plasma etching apparatus.

However, the present invention is not limited to this,

10 but can be applied to another plasma processing apparatus. The technique of decreasing the magnetic field strength on the W pole side so that it is smaller than that on the E pole side is not limited to changing the numbers of anisotropic segment magnets on the two
15 pole sides, as described above. It is understood that another means can be used. For example, the numbers of anisotropic segment magnets are set equal, while the magnetic strengths thereof are made to differ.

Furthermore, regarding the number of anisotropic
20 segment magnets arranged in order to locally increase the magnetic field of a predetermined region, at least one suffices. This number can be arbitrarily selected. This anisotropic segment magnet need not have the same shape and characteristics as those of other anisotropic
25 segment magnets. Also, this anisotropic segment magnet need not be positioned on the same circle where other ones are placed.

The upper electrode is formed by a shower head. Alternatively, the upper electrode may be a mere plate-like electrode. A port through which the process gas is to be introduced may be formed at another location, e.g., in the outer wall of the chamber, and the process gas may be directly supplied into the processing space. In this case, the target substrate can be arranged such that it is supported by the upper electrode.

10

Industrial Applicability

15

20

As has been described above, according to the present invention, a magnetic field forming means can form a strong magnetic field portion locally at a predetermined region located outside that end of a target substrate which is upstream in the electron drift direction. The plasma density of the processing space can accordingly be increased. As a result, when the apparatus of the present invention is used as, e.g., an etching apparatus, uniform etching can be performed under a low pressure. The present invention is thus suitable for etching a device that needs to be micropatterned.

C L A I M S

1. A magnetron plasma processing apparatus which comprising:

5 a chamber with an outer wall that can maintain reduced pressure;

a pair of electrodes arranged in said chamber to oppose each other and to define a processing space therebetween;

10 process gas supply means for supplying a process gas into said chamber;

electric field forming means for applying a voltage to said pair of electrodes, thus forming an electric field in the processing space; and

15 magnetic field forming means for forming, in the processing space, a magnetic field perpendicular to a direction of the electric field and directed in one direction, wherein

20 a target substrate is subjected to a magnetron plasma process while the target substrate is set in the processing space to be parallel to said electrodes,

25 said magnetic field forming means including a dipole ring magnet comprising a plurality of first anisotropic segment magnets arranged in a ring-like shape around the outer wall of said chamber so as to form a magnetic field gradient, in a plane perpendicular to the direction of the electric field, such that a magnetic field strength is large and

small on upstream and downstream sides, respectively,
in an electron drift direction along a direction
perpendicular to a direction of the magnetic field,

at least one second anisotropic segment magnet,
5 arranged in the vicinity of a predetermined region
located outside that end of the target substrate which
is on an upstream side in the electron drift direction,
with an N pole thereof being directed toward the
predetermined region, and

10 at least one third anisotropic segment magnet
arranged in the vicinity of the predetermined region
with an S pole thereof being directed toward the
predetermined region,

said second and third anisotropic segment magnets
15 serving to locally increase a magnetic field of the
predetermined region to be larger than that formed by
said first anisotropic segment magnets.

2. A magnetron plasma processing apparatus
according to claim 1, wherein said second and third
20 anisotropic segment magnets are disposed in a ring-like
shape together with said first anisotropic segment
magnets.

3. A magnetron plasma processing apparatus
according to claim 2, wherein said second and third
25 anisotropic segment magnets are arranged continuously.

4. A magnetron plasma processing apparatus
according to claim 3, wherein said first to third

anisotropic segment magnets are columnar magnets of the same strength which are magnetized in one direction, and said first anisotropic segment magnets are arranged such that directions of magnetization thereof are slightly shifted from each other.

5 5. A magnetron plasma processing apparatus according to claim 4, wherein said first anisotropic segment magnets are arranged such that gaps among those which are on the upstream side in the electron drift
10 direction are narrower than gaps among those which are on the downstream side in the electron drift direction.

 6. A magnetron plasma processing apparatus according to claim 1, wherein said first anisotropic segment magnets form a magnetic field with 200 Gauss at
15 maximum at a portion thereof opposing the target substrate, and said first to third anisotropic segment magnets form a magnetic field of at least 200 Gauss at the predetermined region.

 7. A magnetron plasma processing apparatus
20 comprising:
 a chamber which has an outer wall and can maintain reduced pressure;

 process gas supply means for supplying a process gas into said chamber;

25 a pair of electrodes arranged in said chamber to oppose each other and to define a processing space therebetween;

electric field forming means for applying
a voltage to said pair of electrodes, thus forming
an electric field in the processing space; and

5 magnetic field forming means for forming, in the
processing space between said pair of electrodes,
a magnetic field perpendicular to a direction of the
electric field and directed in one direction, wherein

10 a target substrate is subjected to a magnetron
plasma process while the target substrate is set in
the processing space to be parallel to said electrodes,

15 said magnetic field forming means including
a dipole ring magnet comprising a large number of
anisotropic segment magnets arranged in a ring-like
shape around the outer wall of said chamber, to form
a gradient of the magnetic field, in a plane
perpendicular to the direction of the electric field
between said electrodes, such that a strength of the
magnetic field is large and small on upstream and
downstream sides, respectively, in an electron drift
20 direction along a direction perpendicular to
a direction of the magnetic field,

25 said plurality of anisotropic segment magnets
including a first section comprising at least one
anisotropic segment magnet arranged in the vicinity of
a first region located outside that end of the process
substrate which is on the upstream side in the electron
drift direction with an N pole thereof being directed

toward the first region, and a second section comprising at least one anisotropic segment magnet, arranged in the vicinity of a second region located outside that end of the target substrate which is on the upstream side in the electron drift direction, to be away from the first region, with an S pole thereof being directed toward the second region, the first and second sections serving to locally increase magnetic field strengths of the first and second regions.

8. A magnetron plasma processing apparatus according to claim 7, wherein at least another anisotropic segment magnet is arranged between the first and second sections.

9. A magnetron plasma processing apparatus according to claim 8, wherein said anisotropic segment magnets are columnar magnets of the same strength which are magnetized in one direction, and those of said anisotropic segment magnets which are at a portion other than the first and second portions are arranged such that directions of magnetization thereof are slightly shifted from each other.

10. A magnetron plasma processing apparatus according to claim 9, wherein first anisotropic segment magnets are arranged such that gaps between those which are on the upstream side in the electron drift direction are narrower than gaps between those which are on the downstream side in the electron drift

direction.

11. A magnetron plasma processing apparatus according to claim 7, wherein said anisotropic segment magnets form a magnetic field with 200 Gauss at maximum at a portion thereof opposing the target substrate, and form a magnetic field of at least 200 Gauss at the predetermined region.

12. A magnetron plasma processing apparatus comprising:

10 a chamber to which a process gas is to be supplied;

a pair of electrodes arranged in said chamber to oppose each other and to define a processing space therebetween;

15 electric field forming means for applying a voltage to said pair of electrodes, thus forming an electric field in the processing space; and

magnetic field forming means for forming, in the processing space between said pair of electrodes, a magnetic field perpendicular to a direction of the electric field and directed in one direction, the magnetic field having, at a portion other than that end of a target substrate which is on an upstream side in an electron drift direction, a magnetic field gradient, within a plane perpendicular to the direction of the electric field between said electrodes, such that a magnetic field strength is large and small on

the upstream and downstream sides, respectively, in
the electron drift direction along a direction
perpendicular to a direction of the magnetic field,
the magnetic field strength being locally large at that
5 end on the upstream side, said magnetron plasma
processing apparatus serving to subject the substrate
to a magnetron plasma process while the target
substrate is set in the processing space to be parallel
to said electrodes.

20250403 10:54:13

A B S T R A C T

When a substrate 30 is to be subjected to a magnetron plasma process, a dipole ring magnet 21 is provided, in which a large number of anisotropic segment magnets 22 are arranged in a ring-like shape around the outer wall of a chamber 1. A magnetic field gradient, wherein the magnetic field strength decreases from the E pole side toward the W pole side in a direction perpendicular to a magnetic field direction B, is formed in a plane perpendicular to the direction of an electric field between a pair of electrodes separated from each other. The anisotropic segment magnets have a first section a including anisotropic segment magnets arranged in the vicinity of a region A located outside an E pole side end of the process substrate with an N pole thereof being directed toward this region, and a second portion b including anisotropic segment magnets arranged with an S pole thereof being directed toward this region, to locally increase the magnetic field strengths of the first and second regions.

1/5

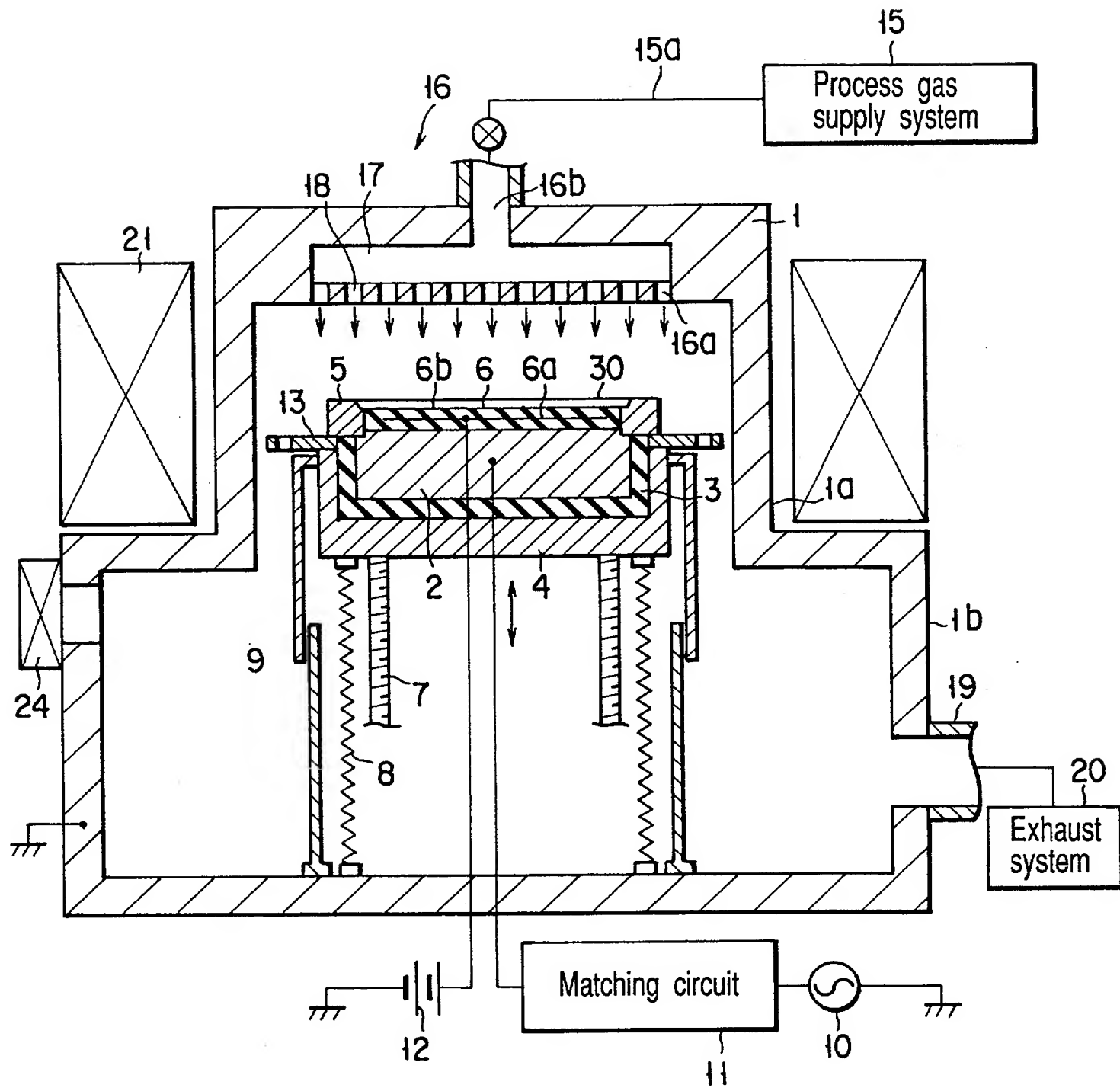


FIG. 1

2/5

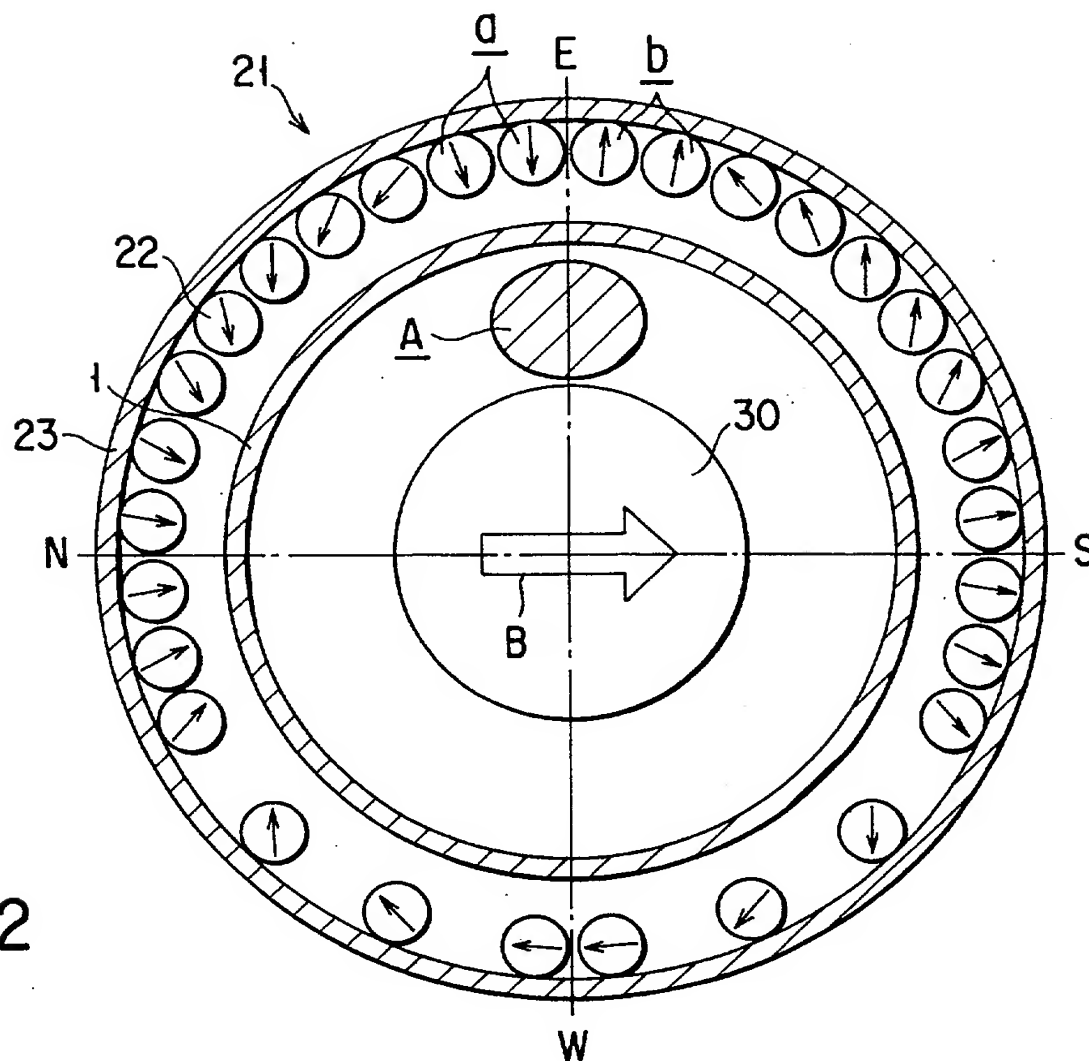


FIG. 2

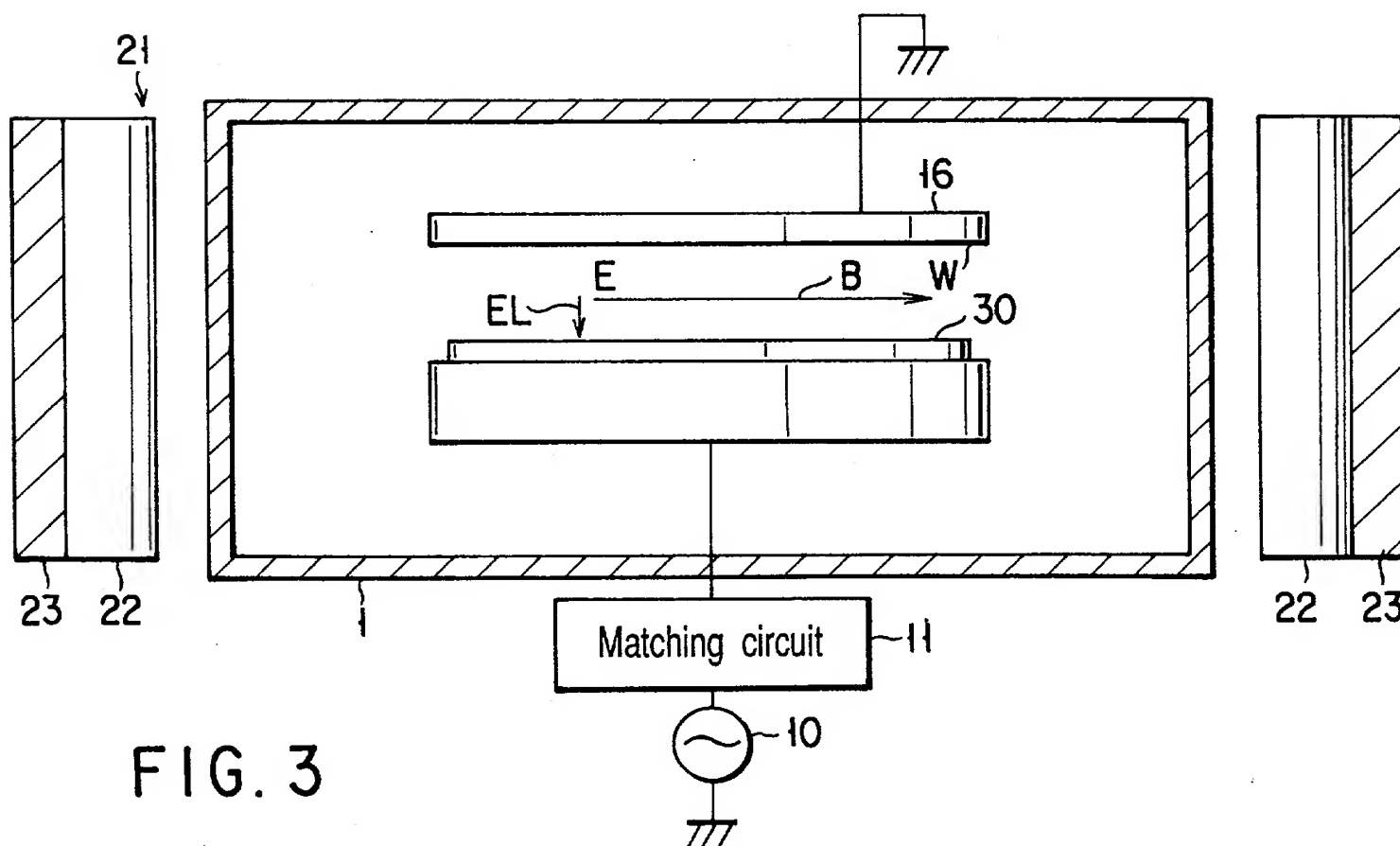


FIG. 3

3/5

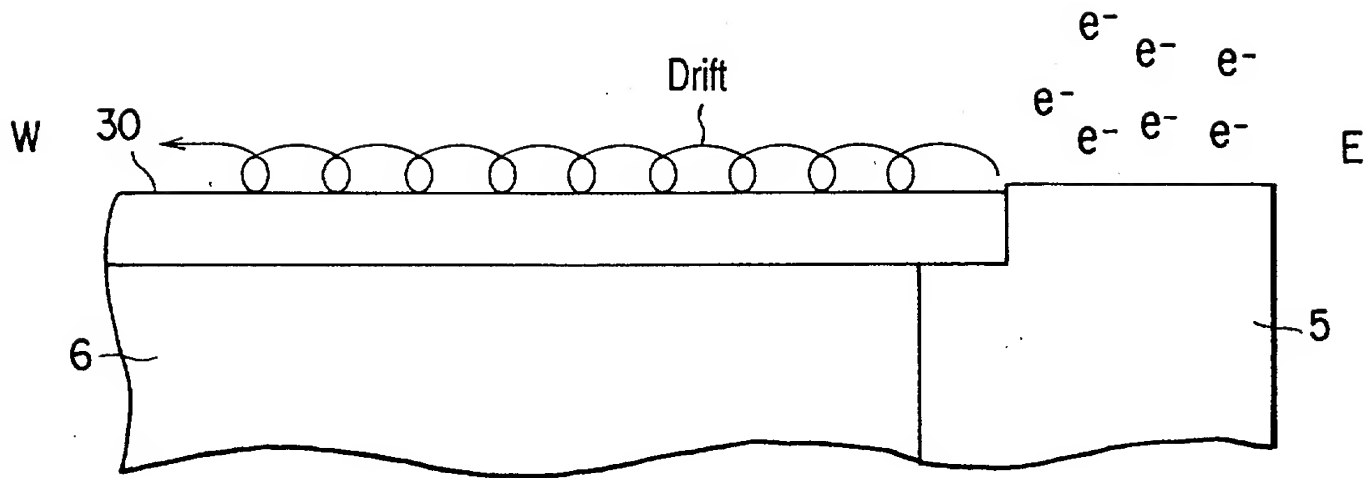


FIG. 4

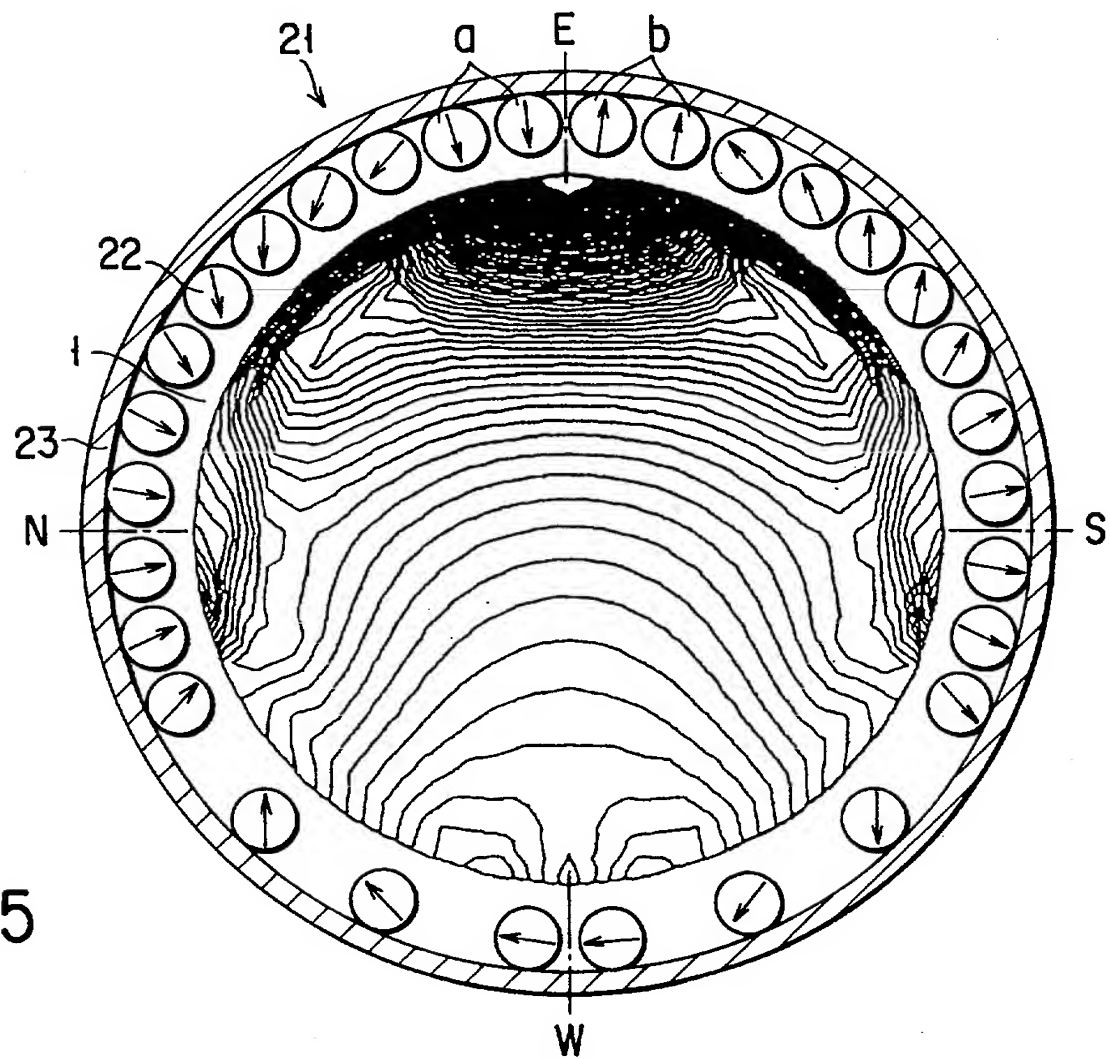


FIG. 5

4/5

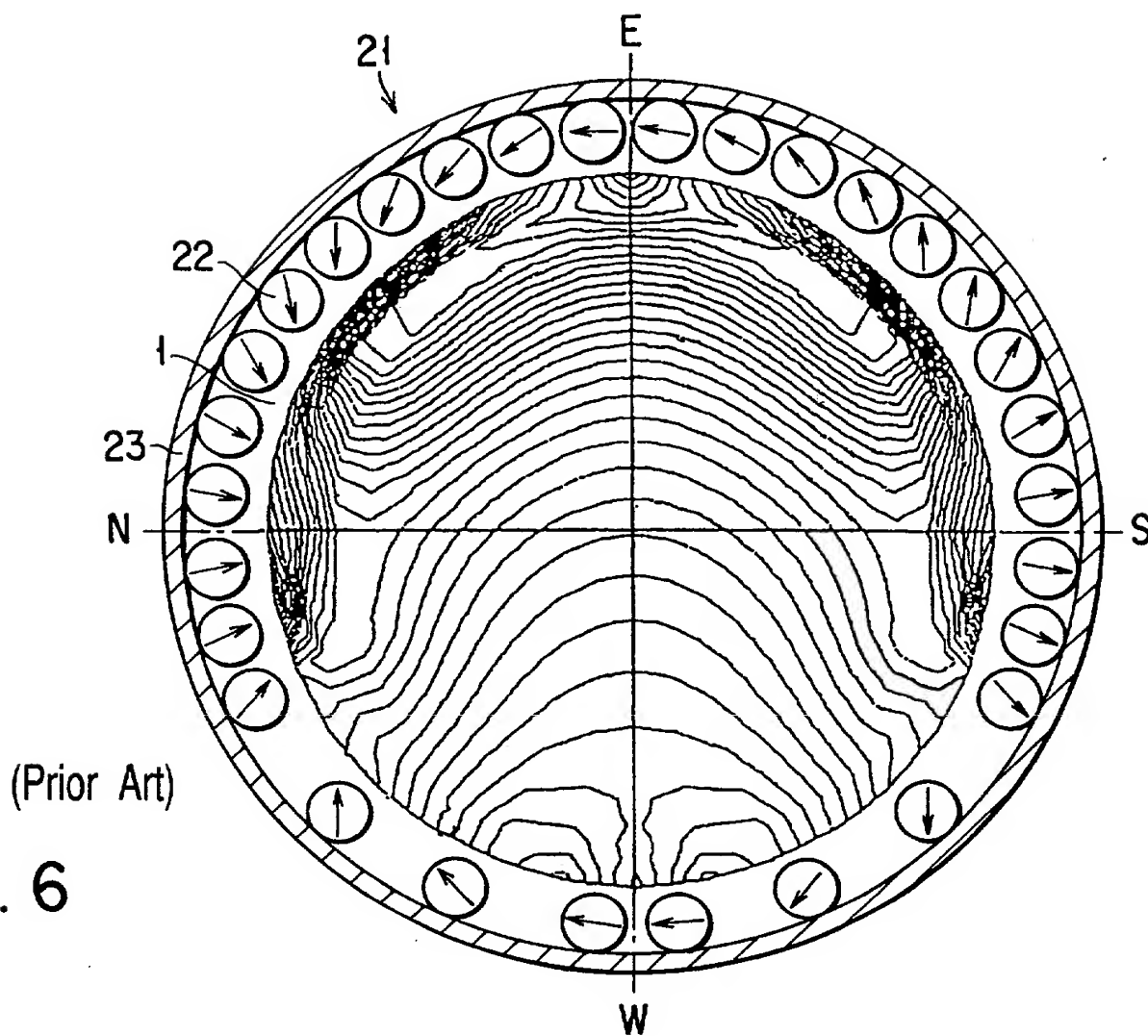


FIG. 6

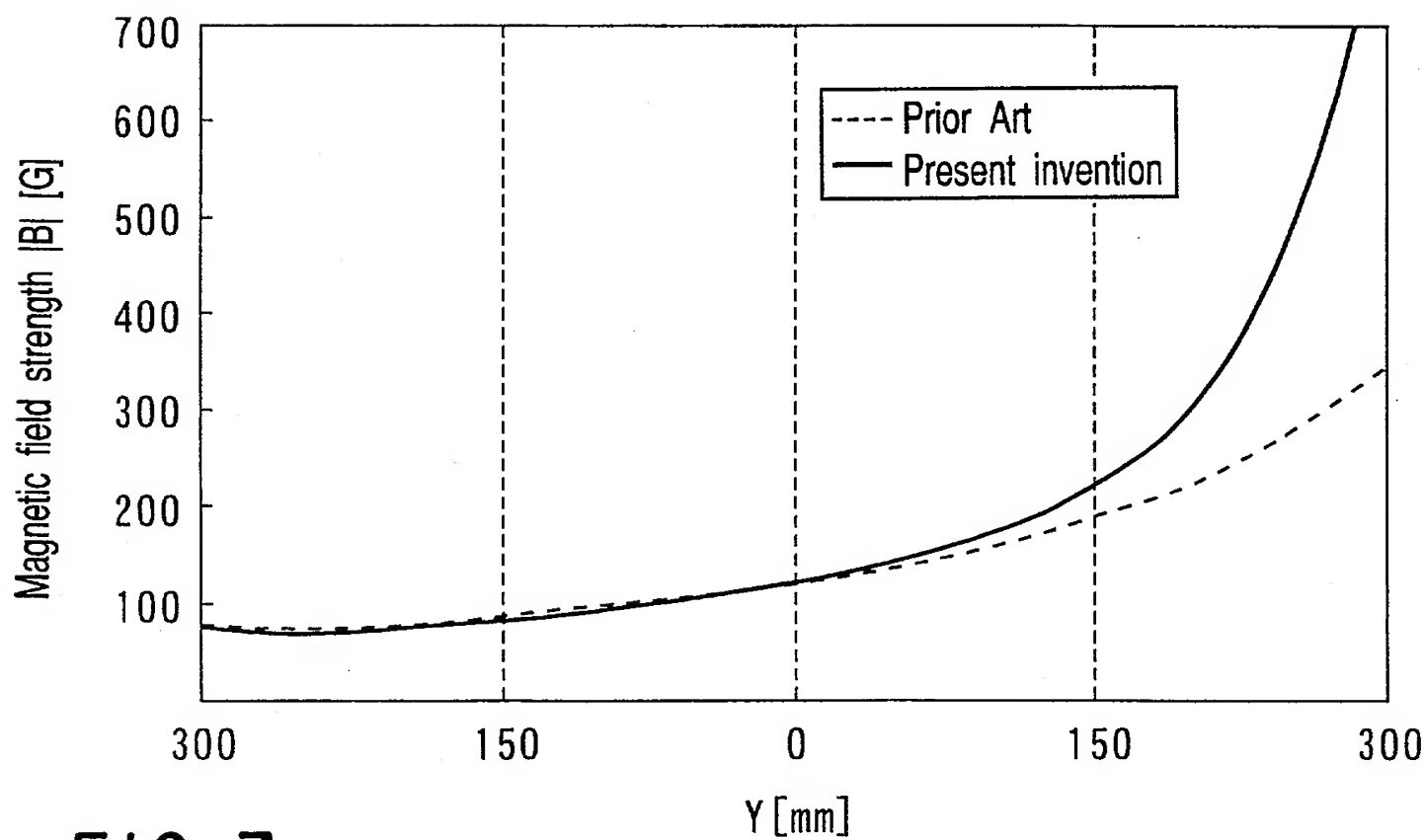


FIG. 7

5/5

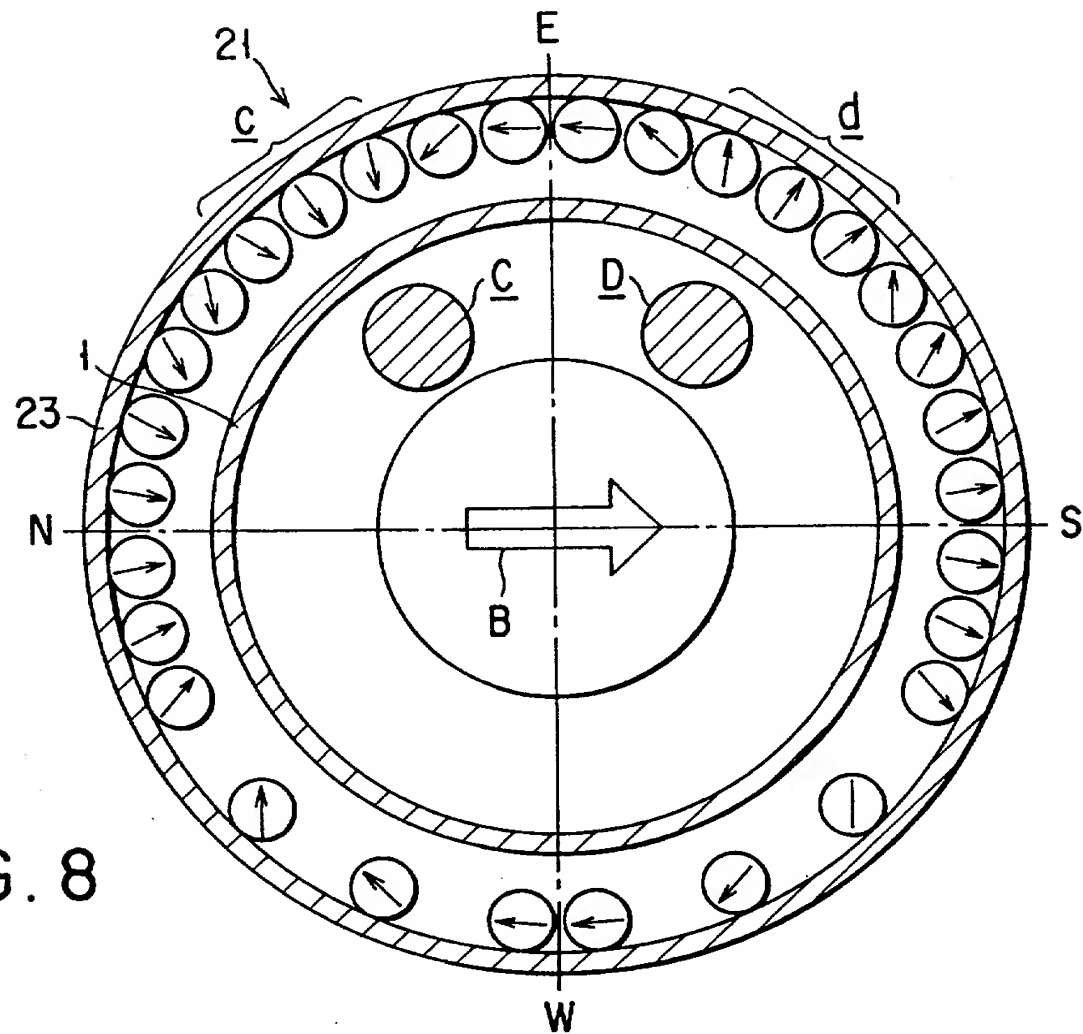


FIG. 8

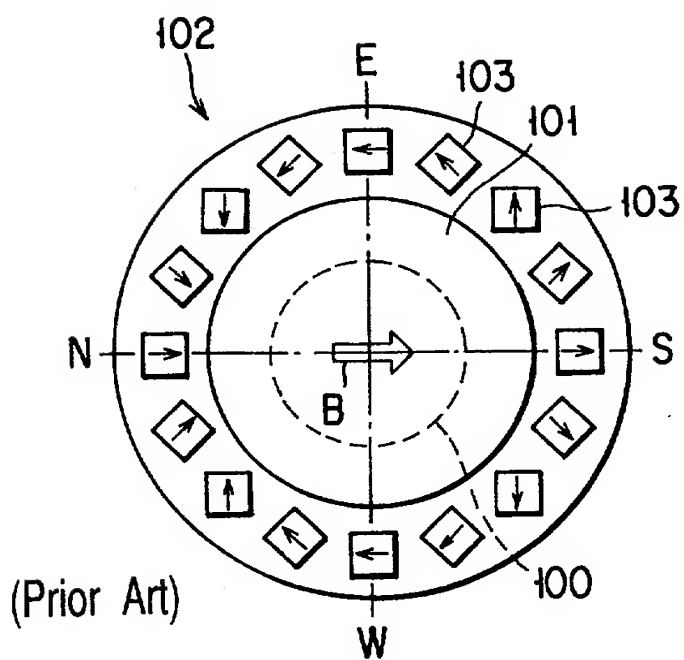


FIG. 9

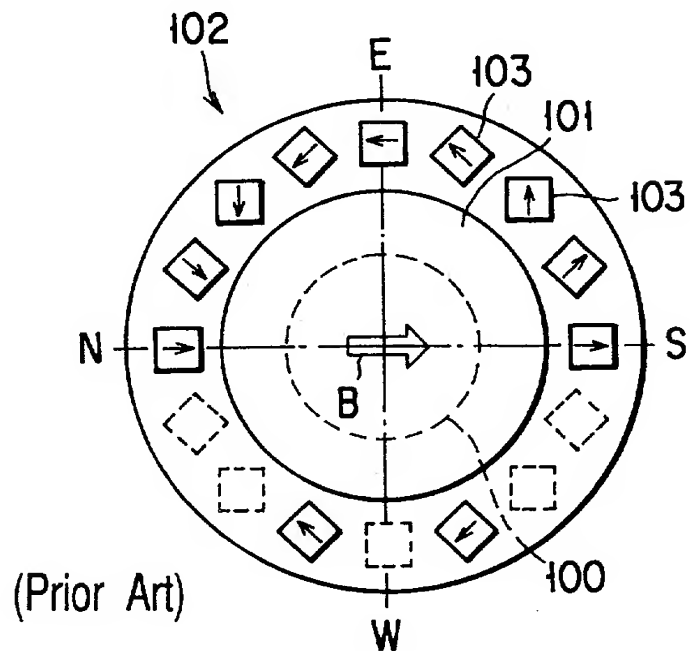


FIG. 10

204030 21359001

Declaration Power of Attorney For Patent Application

特許出願宣言

Japanese Language Declaration

私は、下欄に氏名を記載した発明として、以下の通り宣言する：

私の住所、郵便の宛先および国籍は、下欄に氏名に続いて記載したとおりであり、

名称の発明に関し、請求の範囲に記載した特許を求める主題の本来の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本来の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

マグネトロンプラズマ処理装置

その明細書を
（該当するほうに印を付す）

☐ ここに添付する。

☒ 2000, 8 月 28 日に

出願番号第 PCT/JP00/05798 号として
提出し、 月 日に補正した。
（該当する場合）

私は、前記のとおり補正した請求の範囲を含む前記明細書の内容を検討し、理解したことを陳述する。

私は、連邦規則法典第37部第1章第56条（a）項に従い、本願の審査に所要の情報を開示すべき義務を有することを認める。

As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

MAGNETRON PLASMA PROCESSING
APPARATUS

The specification of which
(check one)

☐ is attached hereto.

☒ was filed on AUGUST 28, 2000

as Application No.

PCT/JP00/05798 and was amended on
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

Japanese Language Declaration

私は、合衆国法典第35部第119条、第172条、又は第365条に基づく下記の外国特許出願又は発明者証出願の外国優先権利益を主張し、さらに優先権の主張に係わる基礎出願の出願日前の出願日を有する外国特許出願又は発明者証出願を以下に明記する：

I hereby claim foreign priority benefits under Title 35, United States Code Sec. 119, Sec. 172 or Sec. 365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior foreign application(s)
先の外国出願

Priority Claimed
優先権の主張

11-248693	JAPAN	02/09/1999	<input checked="" type="checkbox"/>	<input type="checkbox"/>
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
(番号)	(国名)	(出願年月日)	あり	なし
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>

私は、合衆国法典第35部第120条に基づく下記の合衆国特許出願の利益を主張し、本願の請求の範囲各項に記載の主題が合衆国法典第35部第112条第1項に規定の態様で先の合衆国出願に開示されていない限度において、先の出願の出願日と本願の国内出願日又はPCT国際出願日の間に公表された連邦規則法典第37部第1章第56条(a)項に記載の所要の情報を開示すべき義務を有することを認める。

I hereby claim the benefit of Title 35, United States Code, Sec. 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Sec. 112, I acknowledge the duty to disclose any material information as defined in Title 37, Code of Federal Regulations, Sec. 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application No.) (出願番号)	(Filing Date) (出願日)	(Status: Patented, Pending, Abandoned) (現況: 特許許可済、係属中、放棄済)
(Application No.) (出願番号)	(Filing Date) (出願日)	(Status: Patented, Pending, Abandoned) (現況: 特許許可済、係属中、放棄済)

私は、ここに自己の知識に基づいて行った陳述がすべて真実であり、自己の有する情報及び信ずるところに従って行った陳述で真実であると信じ、更に故意に虚偽の陳述等を行った場合、合衆国法典第18部第1001条により、罰金もしくは禁固に処せられるか、又はこれらの刑が併科され、又はかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損なうことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true; and further that all statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

100644-030400

Japanese Language Declaration

(日本語宣言書)

委任状：私は、下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。
(弁理士、または代理人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

I hereby appoint the registrants of Oblon, Spivak, McClelland, Maier & Neustadt, P.C., Fourth Floor, 1755 Jefferson Davis Highway, Arlington, Virginia 22202, Customer No. 22850, or any one of them. Send correspondence to Oblon, Spivak, McClelland, Maier & Neustadt, P. C., Fourth Floor, 1755 Jefferson Davis Highway, Arlington, Virginia 22202, Telephone No. (703)413-3000.

書類送付先：

Send Correspondence to
Oblon, Spivak, McClelland,
Maier & Neustadt, P.C.,
Fourth Floor,
1755 Jefferson Davis Highway,
Arlington, Virginia 22202

直通電話連絡先：

Direct Telephone Calls to: (name and telephone number)
Telephone No. (703)413-3000
Facsimile No. (703)413-2220

唯一のまたは第一発明者の氏名 山崎 智美	Full name of sole or first inventor Tomomi Yamasaki
同発明者の署名 日付	Inventor's signature Date Tomomi Yamasaki February 14, 2002
住 所 日本国姫路市	Residence Himeji-shi, Japan JPX
国 籍 日本	Citizenship JAPAN
郵便の宛先 〒670-0057 日本国兵庫県姫路市 北今宿2丁目5番の5-301号	Mailing Address 5-5-301, Kitaimajuku 2-chome, Himeji-shi, Hyogo 670-0057 Japan

(第二以降の共同発明者に対しても同様に記載し、署名をすること。)

(Supply similar information and signature for second and subsequent joint inventors.)

Japanese Language Declaration

(日本語宣言書)

第 2 共同発明者の氏名 (該当する場合) 木村 英利	Full name of 2nd joint inventor, if any Hidetoshi Kimura 2nd
同第 2 発明者の署名 日付	2nd inventor's signature Date February 15, 2002 Hidetoshi Kimura
住 所 日本国韭崎市	Residence Nirasaki-shi, Japan JPX
国 籍 日本	Citizenship JAPAN
郵便の宛先 〒107-8481 日本国東京都港区赤坂5丁目3番6号	Mailing Address c/o Intellectual Property Dept., TOKYO ELECTRON LIMITED
東京エレクトロン株式会社 知的財産部内	3-6, Akasaka 5-chome, Minato-ku, Tokyo 107-8481, Japan
第 3 共同発明者の氏名 (該当する場合) 荒見 淳一	Full name of 3rd joint inventor, if any Junichi Arami 3rd
同第 3 発明者の署名 日付	3rd inventor's signature Date February 19, 2002 Junichi Arami
住 所 日本国東京都	Residence Tokyo, Japan JPX
国 籍 日本	Citizenship JAPAN
郵便の宛先 〒107-8481 日本国東京都港区赤坂5丁目3番6号	Mailing Address c/o Intellectual Property Dept., TOKYO ELECTRON LIMITED
東京エレクトロン株式会社 知的財産部内	3-6, Akasaka 5-chome, Minato-ku, Tokyo 107-8481, Japan
第 4 共同発明者の氏名 (該当する場合) 小野 博夫	Full name of 4th joint inventor, if any Hiroo Ono 4th
同第 4 発明者の署名 日付	4th inventor's signature Date February 15, 2002 Hiroo Ono
住 所 日本国韭崎市	Residence Nirasaki-shi, Japan JPX
国 籍 日本	Citizenship JAPAN
郵便の宛先 〒107-8481 日本国東京都港区赤坂5丁目3番6号	Mailing Address c/o Intellectual Property Dept., TOKYO ELECTRON LIMITED
東京エレクトロン株式会社 知的財産部内	3-6, Akasaka 5-chome, Minato-ku, Tokyo 107-8481, Japan

200604040001

Japanese Language Declaration

(日本語宣言書)

第 5 共同発明者の氏名 (該当する場合)	Full name of 5th joint inventor, if any
興石 公	Akira Koshiishi 50
同第 5 発明者の署名 日付	5th inventor's signature Date
	Akira Koshiishi February 15, 2002
住 所	Residence
日本国斐崎市	Nirasaki-shi, Japan SPX
国 籍	Citizenship
日本	JAPAN
郵便の宛先 〒107-8481 日本国東京都港区赤坂5丁目3番6号	Mailing Address c/o Intellectual Property Dept., TOKYO ELECTRON LIMITED
東京エレクトロン株式会社 知的財産部内	3-6, Akasaka 5-chome, Minato-ku, Tokyo 107-8481, Japan
第 6 共同発明者の氏名 (該当する場合)	Full name of 6th joint inventor, if any
宮田 浩二	Koji Miyata 60
同第 6 発明者の署名 日付	6th inventor's signature Date
	Koji Miyata February 18, 2002
住 所	Residence
日本国武生市	Takefu-shi, Japan SPX
国 籍	Citizenship
日本	JAPAN
郵便の宛先 〒915-0802 日本国福井県武生市北府2丁目1番5号 信越化学工業株式会社磁性材料研究所内	Mailing Address c/o Shin-Etsu Chemical Co., Ltd. Magnetic Materials Research Center, 1-5, Kitago 2-chome, Takefu-shi, Fukui 915-0802 Japan
第 共同発明者の氏名 (該当する場合)	Full name of joint inventor, if any
同第 発明者の署名 日付	inventor's signature Date
住 所	Residence
国 籍	Citizenship
郵便の宛先	Mailing Address

20060215 09:40